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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent application of:  
Ferro et al.

Serial No.: 10/060,750

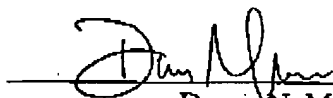
Filed: January 30, 2002

Group Art Unit: 2123

Examiner: Sharon, Ayal I.

Atty. Docket No.: BUR920010016US1

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571-273-8300) on 3/30/06

Duane N. Moore

For: SYSTEM FOR CONTROLLING EXTERNAL MODELS USED FOR  
VERIFICATION OF SYSTEM ON A CHIP (SOC) INTERFACES

## RESPONSE UNDER 37 C.F.R. §1.116

Mail Stop Non-Fee Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

This is in response to the Office Action mailed February 14, 2006, setting a three-month statutory period for response. Therefore, this response is timely filed. Please consider the following.

10/060,750

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